



Suresh Angadi Education Foundation's  
ANGADI INSTITUTE OF TECHNOLOGY & MANAGEMENT, BELAGAVI-590 009  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
(NAAC ACCREDITED)  
TIME - TABLE - 2021-22 (ODD SEM)



73

Faculty coordinator: Sreedevi Kulkarni

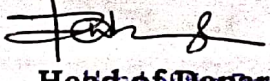
Semester: 3

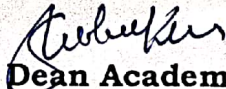
Class Room: ECLH-01

Time/Day	09.00 - 10.00	10.00 - 11.00	11.00 - 11.15	11.15 - 12.15	12.15 - 01.15	01.15 - 02.15	02.15 - 03.15	03.15 - 04.15	04.15 - 5.00
Mon	DSD	PEI	T E A B R E A K	MATH-III	NT	L U N C H  B R E A K	B1-EDI LAB B2-DSD LAB		
Tue	NT	DSD		ED	CO		B2-EDI LAB B1-DSD LAB		
Wed	MATH-III	NT		PEI	ED		<.....VYAVAHARIKA KANNADA.....>		
Thu	NT	ED		CO	MATH-III		DSD	CO	PEI
Fri	CO	ED		DSD	PEI		ED	NT	MATH-III
Sat	DSD	PEI		CO	MATH-III				

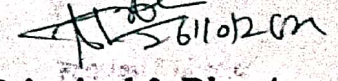
Sub. Code	Sub-Abrr	Subject Title	Faculty In charge
18MAT31	MAT	ENGG. MATHEMATICS - III	PROF. ASHFAK PATHAN
18EC32	NT	NETWORK THEORY	PROF. ANILKUMAR C.K.
18EC33	ED	ELECTRONIC DEVICES	PROF. RAVIRAJ CHOUGALA
18EC34	DSD	DIGITAL SYSTEM DESIGN	PROF. SREEDEVI KULKARNI
18EC35	COA	COMPUTER ORGANIZATION & ARCHITECTURE	PROF. ARAVIND JADHAV
18EC36	PEI	POWER ELECTRONICS & INSTRUMENTATION	DR. SANJAY A. PUJARI
18ECL37	EDI LAB	ELECTRONIC DEVICES & INSTRUMENTATION LAB	PROF. ANILKUMAR C. K.
18ECL38	DSD LAB	DIGITAL SYSTEM DESIGN LAB	PROF. SREEDEVI KULKARNI
18KVK39/18KAK39	KVK/KAK	VYAVAHARIKA KANNADA	PROF. ANILKUMAR C. K.

  
Academic Coordinator

  
Head of Department  
Electronics & Communication Engineering  
AITM, Belagavi.

  
Dean Academics

  
IQAC Coordinator  
ANGADI INSTITUTE OF  
TECHNOLOGY & MANAGEMENT,  
BELAGAVI

  
Principal & Director  
ANGADI INSTITUTE OF TECHNOLOGY & MANAGEMENT,  
BELAGAVI





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 TIME - TABLE - 2021-22 (ODD SEM)



Faculty coordinator: Sriram K V

Semester: 5

Class Room: ECLH-03

Time/Day	09.00 - 10.00	10.00 - 11.00	11.00 - 11.15	11.15 - 12.15	12.15 - 01.15	01.15 - 02.15	02.15- 03.15	03.15 - 04.15	04.15 - 5.00	
Mon	DSP	ITC	T E A  B R E A K	EMW	VHDL	L U N C H  B R E A K	PCS	VHDL	TIM&E	
Tue	VHDL	ITC		TIM&E	EMW		DSP	TIM&E	ITC	
Wed	ITC	VHDL		DSP	PCS		B1-DSP LAB B2-VERILOG HDL LAB B2-DSP LAB B1-VERILOG HDL LAB			
Thu	EMW	PCS		ITC	TIM&E					
Fri	DSP	EMW		PCS	VHDL					
Sat	TIM&E	PCS		DSP	EMW					

Sub. Code	Sub-Abrr	Subject Title	Faculty In charge
18ES51	TIM&E	TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	PROF.GOUTAM KALLIMANI
18EC52	DSP	DIGITAL SIGNAL PROCESSING	PROF.GAJANAN TUDAVEKAR
18EC53	PCS	PRINCIPLES OF COMMUNICATION SYSTEMS	PROF. ARAVIND JADHAV
18EC54	ITC	INFORMATION THEORY & CODING	PROF. RAVIRAJ CHOUGALA
18EC55	EMW	ELECTROMAGNETIC WAVES	PROF. SREEDEVI KULKARNI.
18EC56	VHDL	VERILOG HDL	PROF. SRIRAM K. V.
18ECL57	DSP LAB	DIGITAL SIGNAL PROCESSING LABORATORY	PROF.GAJANAN TUDAVEKAR
18ECL58	VHDL LAB	VERILOG HDL LAB	DR.ANAND DESHPANDE/ PROF. SRIRAM K. V.

*Sriram K V*  
 Academic Coordinator

*Sriram K V*  
 Head of Department  
 Head of the Department  
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 AITM, Belagavi.

*Sriram K V*  
 Dean Academics

*Sriram K V*  
 IQAC Coordinator  
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TIME - TABLE - 2021-22 (ODD SEM)

Faculty coordinator: Gautam Kallimani

Semester: 7

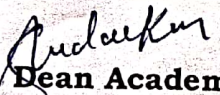
Class Room: ECLH-02

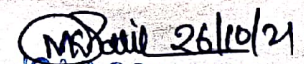
Time/Day	09.00 - 10.00	10.00 - 11.00	11.00 - 11.15	11.15 - 12.15	12.15 - 01.15	01.15 - 02.15	02.15- 03.15	03.15 - 04.15	04.15 - 5.00
Mon	VLSI	AI	T E A C H E R S B R E A K	CN	IOT&WSN	L U N C H  B R E A K	B1-CN LAB B2-VLSI LAB		
Tue	RTS	AI		CN	RTS		B2-CN LAB B3-VLSI LAB		
Wed	IOT&WSN	AI		IOT&WSN	VLSI		B3-CN LAB B1-VLSI LAB		
Thu	CN	AI		RTS	VLSI		PROJECT WORK PHASE-I + PROJECT WORK SEMINAR		
Fri	CN	VLSI		IOT&WSN	RTS		PROJECT WORK PHASE-I + PROJECT WORK SEMINAR		
Sat	VLSI	RTS		CN	IOT&WSN				

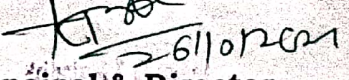
Sub. Code	Sub-Abrr	Subject Title	Faculty In charge
18EC71	CN	COMPUTER NETWORKS	DR.SANJAY A. PUJARI
18EC72	VLSI	VLSI DESIGN	PROF.ANILKUMAR C K
18EC731	RTS	REAL TIME SYSTEM (Professional Elective-II)	PROF VISHALKIRTHI PATIL
18EC741	IOT&WSN	IOT&WIRELESS SENSOR NETWORKS (Professional Elective-III)	PROF.GOUTAM KALLIMANI
18CS753	AI	INTRODUCTION TO ARTIFICIAL INTELLIGENCE (Open Elective-I)	DR. ANAND DESHAPANDE
18ECL76	CN LAB	COMPUTER NETWORK LAB	DR.SANJAY A. PUJARI
18ECL77	VLSI LAB	VLSI LAB	PROF. RAVIRAJ CHOUGALA.
18ECP78	-----	PROJECT WORK PHASE-I + PROJECT SEMINAR	

  
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Dean Academics

  
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Principal & Director